

FIG. 1

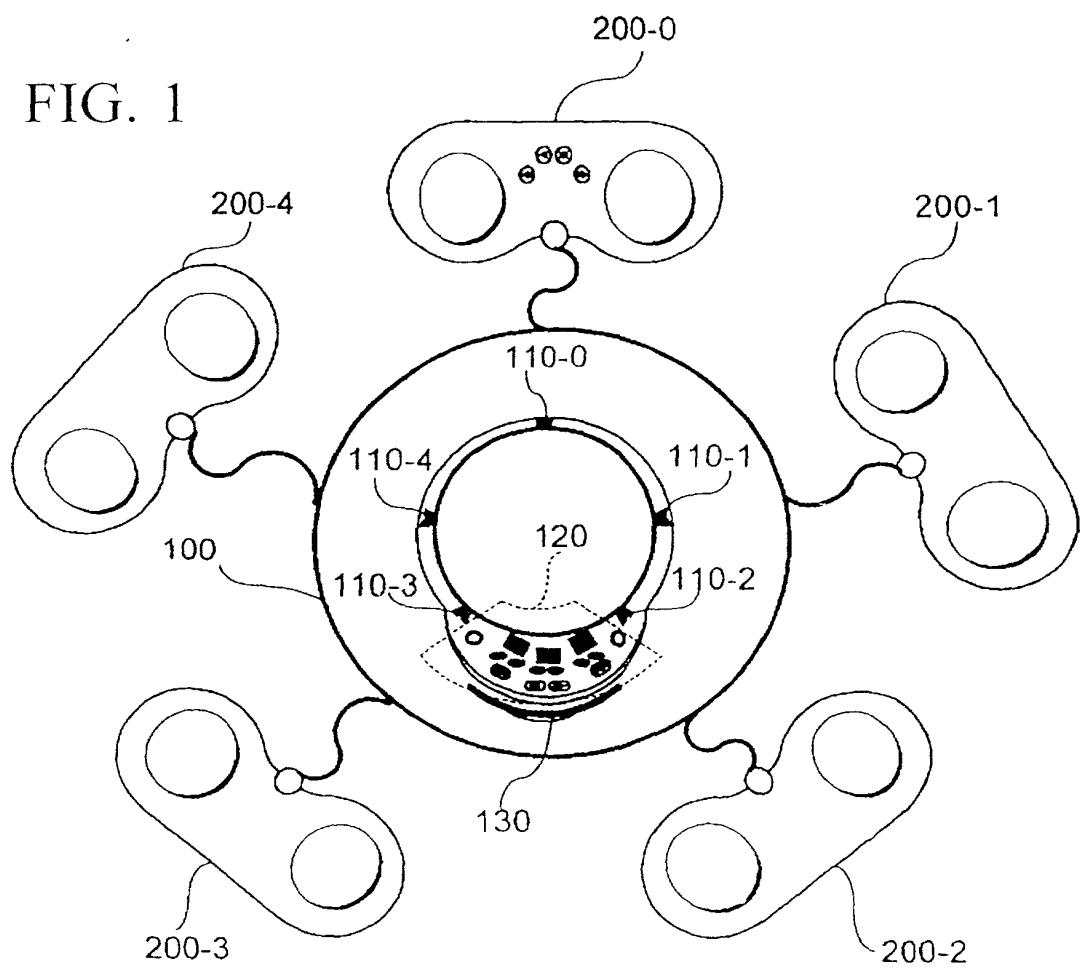


FIG. 2

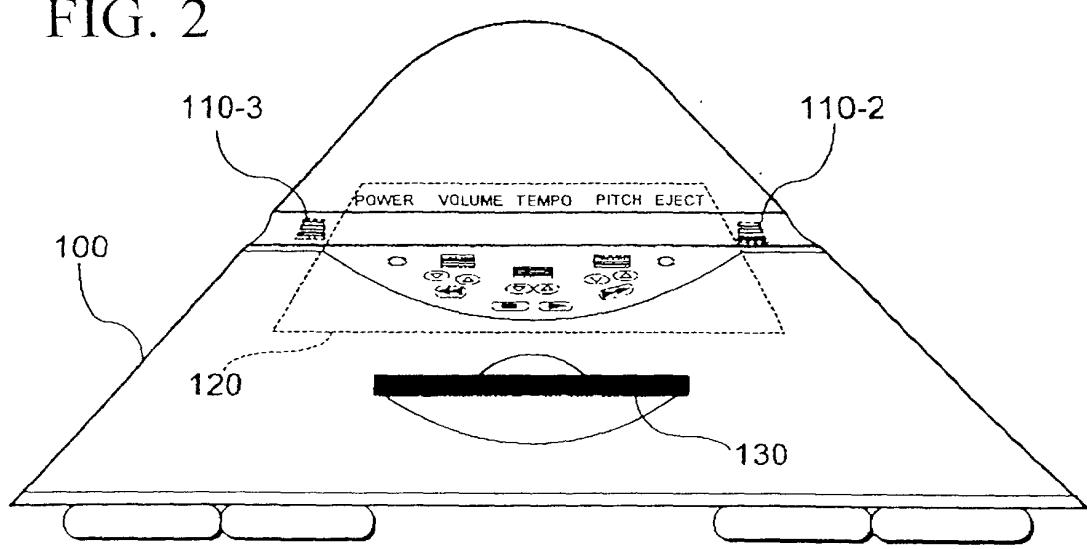
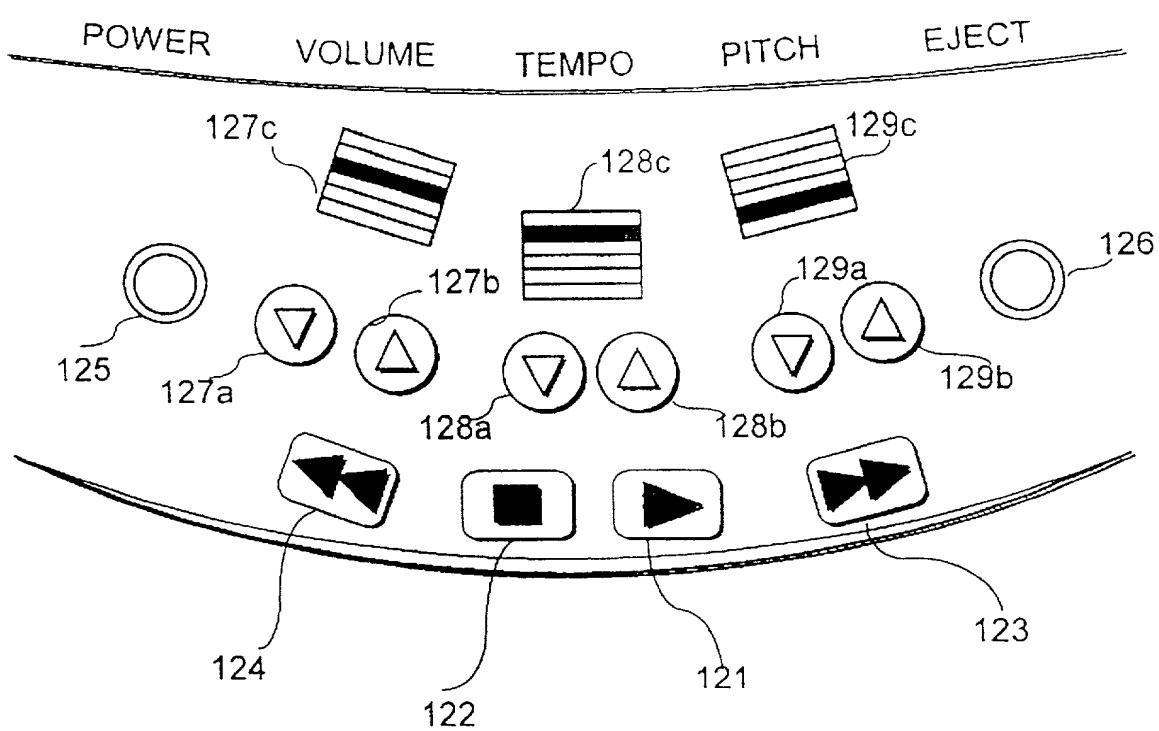


FIG. 3



120; CONTROL PANEL

FIG. 4

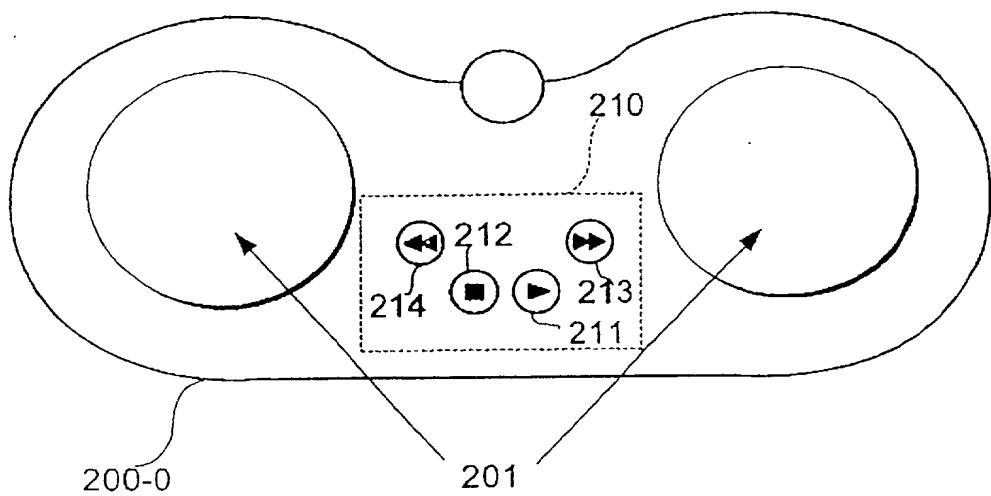


FIG. 5

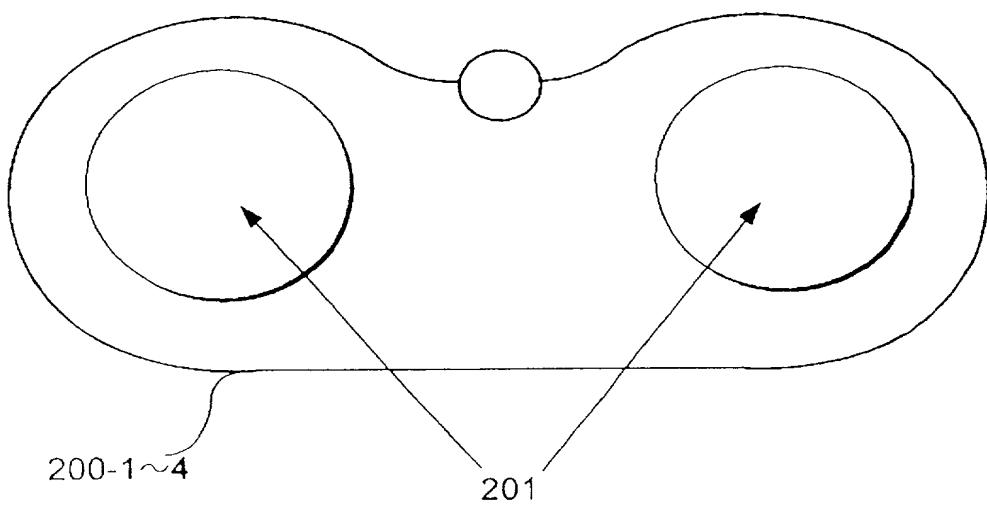


FIG. 6

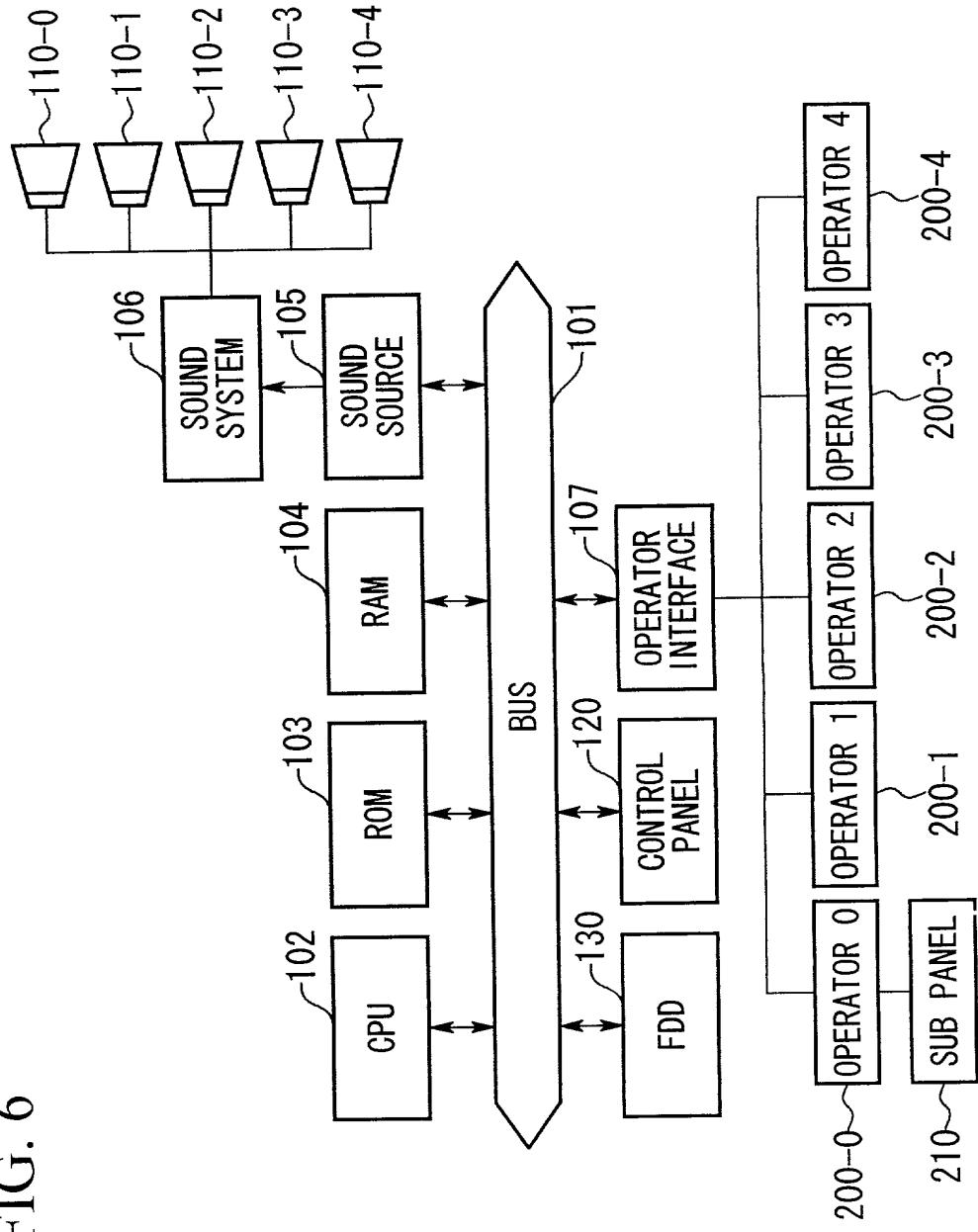


FIG. 7

ROM 103

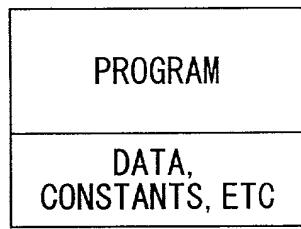
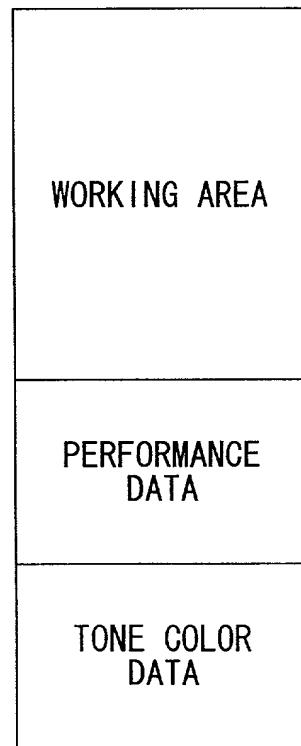


FIG. 8

RAM 104



00020000 00000000 00000000 00000000

FIG. 9

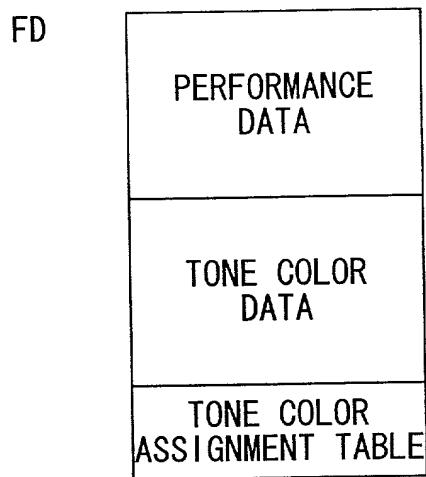


FIG. 10

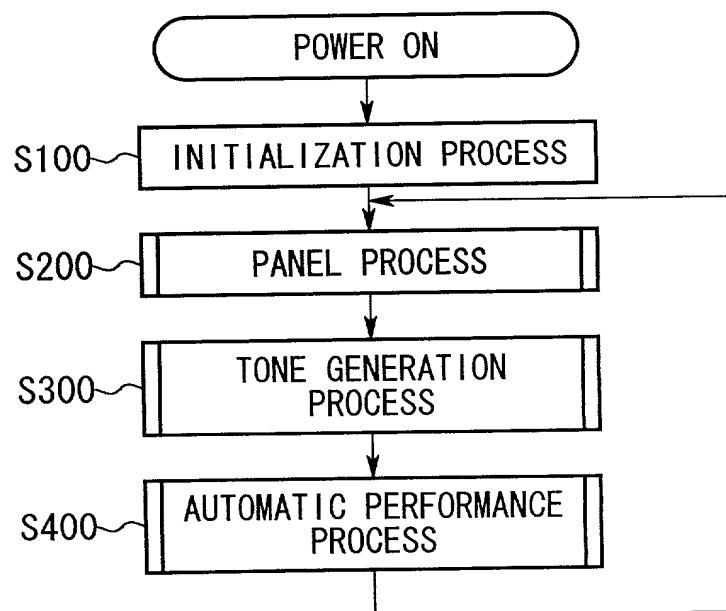


FIG. 11

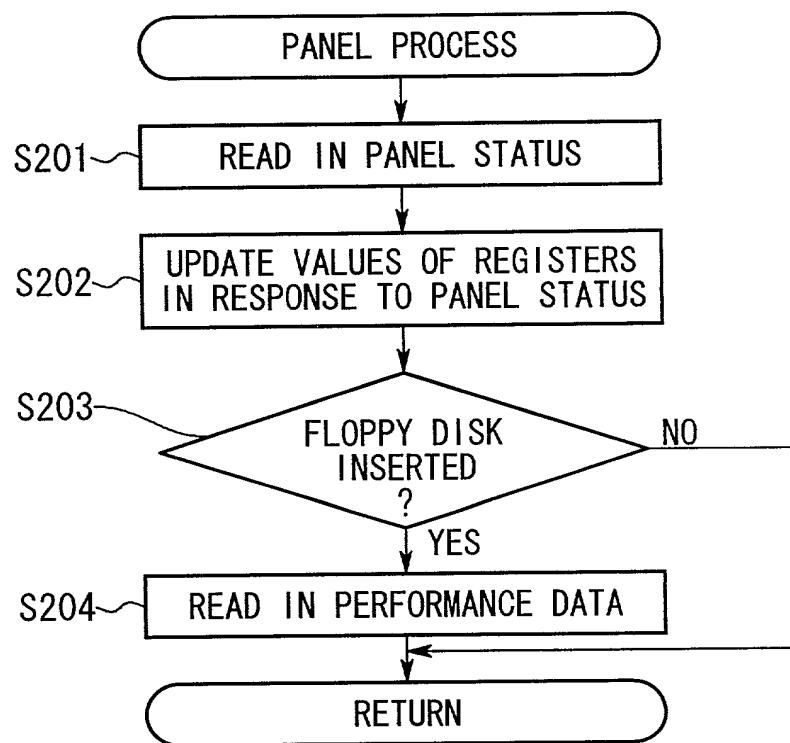


FIG. 12

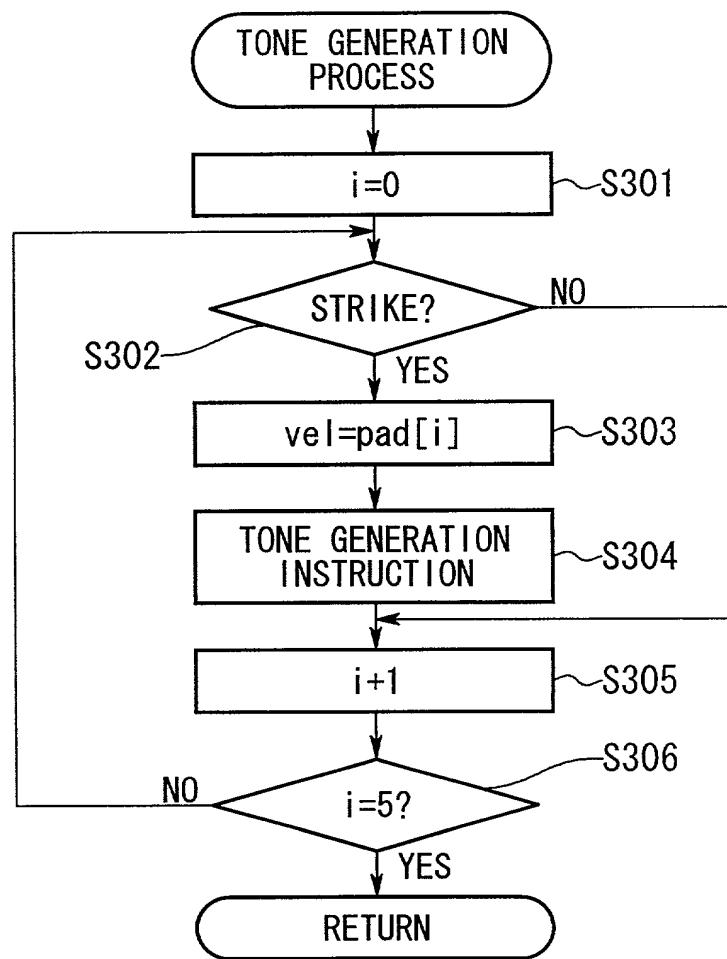


FIG. 13

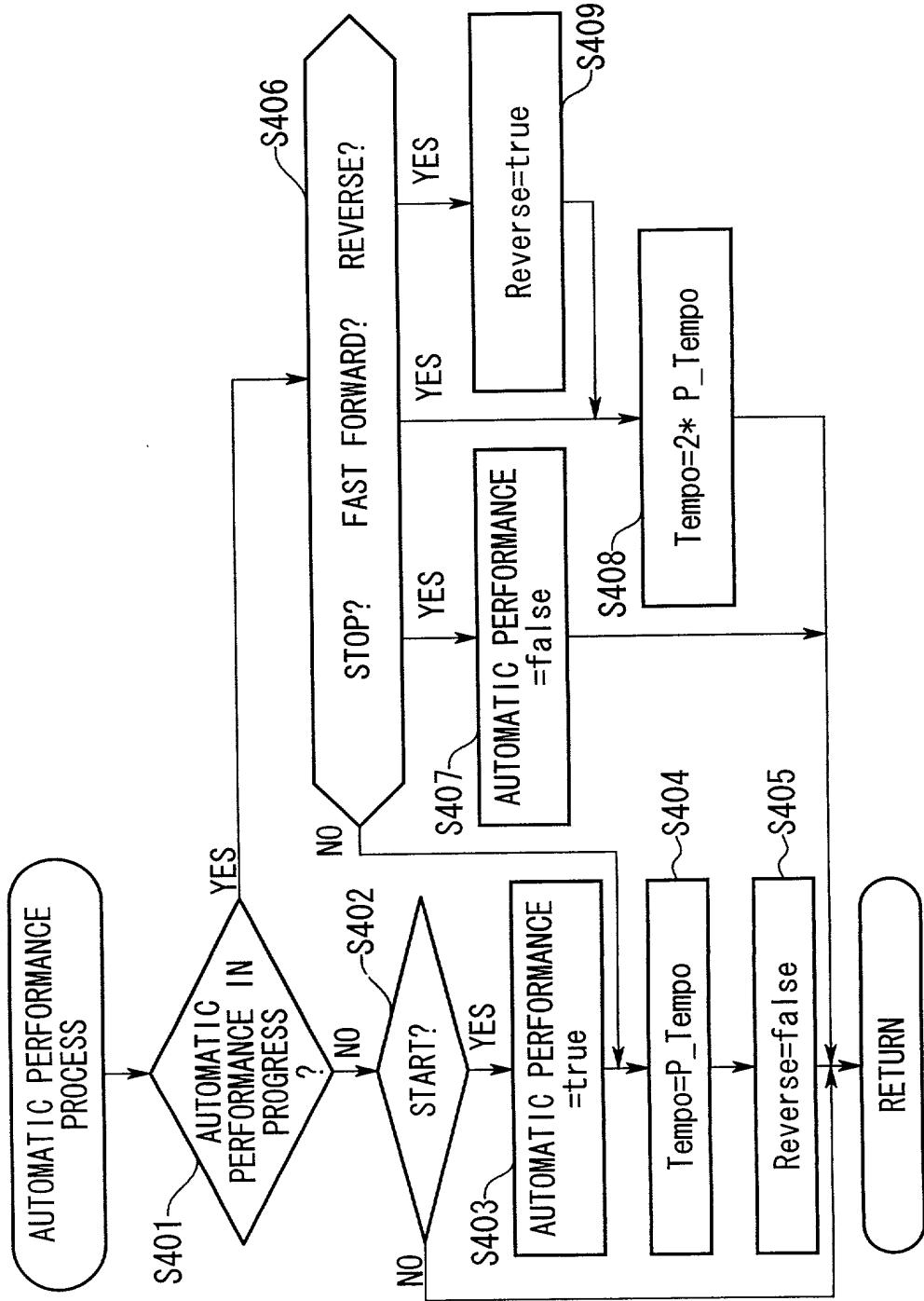


FIG. 14

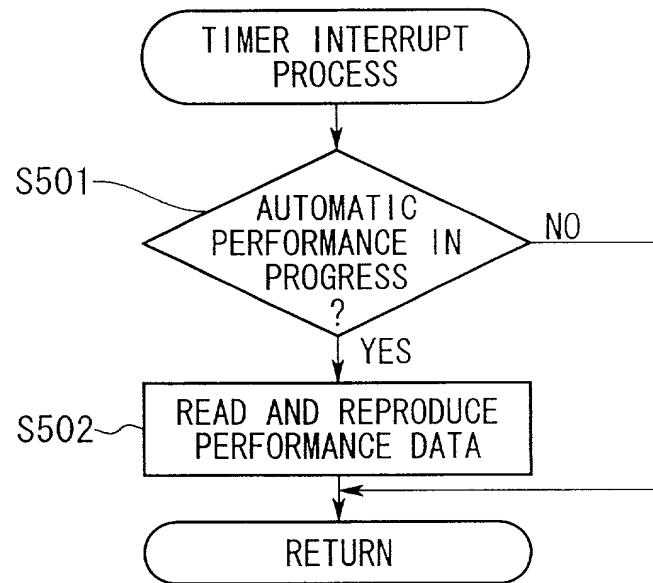


FIG. 15

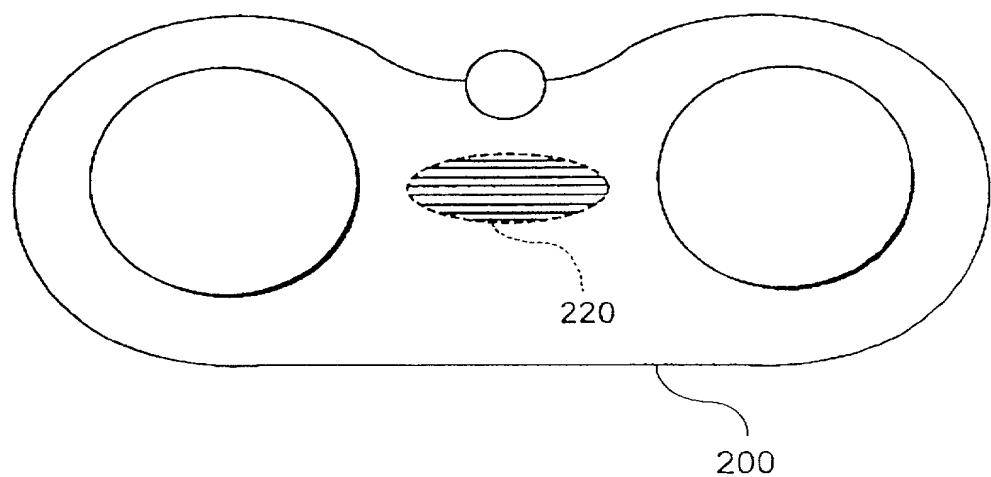


FIG. 16

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